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				Application Number	09/832,844
				Filing Date	April 12, 2001
				First Named Inventor	Shunpei YAMAZAKI
				Group Art Unit	2826
				Examiner Name	M. Tran
Sheet	1	of	1	Attorney Docket Number	0756-2288

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MLT		H. YANAI, 3.3.2 Basic Circuit of Analog Switch, Semiconductor Handbook, 2 nd Edition, Pages 842-843, 1977.	Concise Statement
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